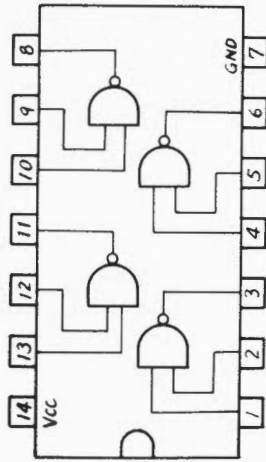


MODEL 925

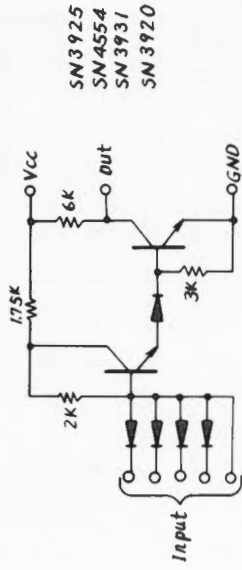
LOGIC & TIMING CHARTS

TABLE OF CONTENTS

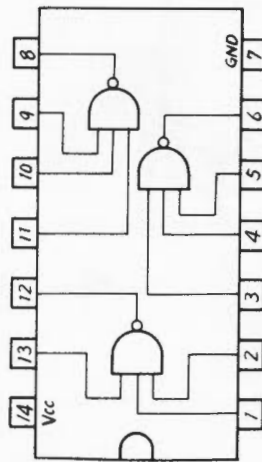
IC LOGIC CIRCUIT	1
TIMING CHART	2
FLOW CHART, ENTRY, ADD & SUB	3
FLOW CHART MLT	4
FLOW CHART DIV	5
KEY CIRCUIT	6
INDICATION CIRCUIT	7
OSC, FTS, FD & TD	8
FSD & FID	9
FMLT, FDIV, FOF, FJ, FN & FG	10
PC & SCT	11
CONTROL COUNTER.	12
GATE CIRCUIT	13
WRITE & READ AMP	14
ADDER & FAD	15
POWER SUPPLY CIRCUIT (115V UL)	16
POWER SUPPLY CIRCUIT (EXCEPT 115V UL)	17
TRANSFORMER TERMINAL DIAGRAM	18
TIMING CHART ENTRY-1	19
TIMING CHART ENTRY-2	20
TIMING CHART ADD & SUB	21
TIMING CHART MLT-1	22
TIMING CHART MLT-2	23
TIMING CHART DIV-1	24
TIMING CHART DIV-2	25
TIMING CHART DIV-3	26
TIMING CHART DIV-4	27
BLOCK DIAGRAM.	28



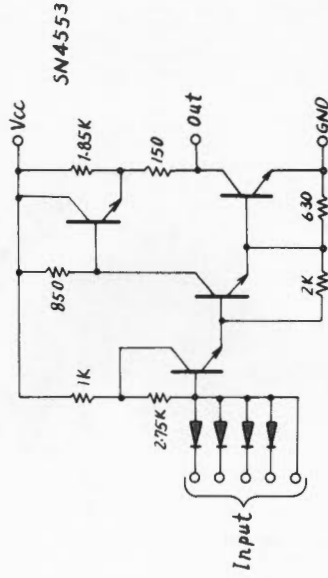
SN 3925 (X 65-7001) DTL
 SN 4554 (X 65-7004) DTL
 SN 7400 (X 65-7011) TTL



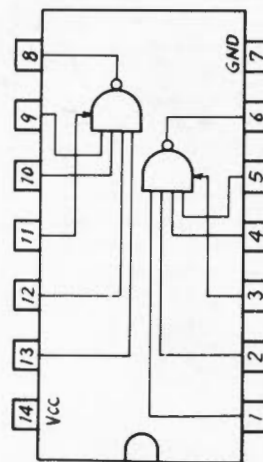
SN 3925
 SN 4554
 SN 3931
 SN 3920



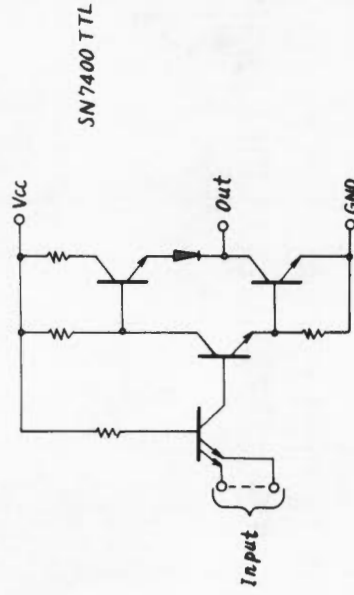
SN 3931 (X 65-7002) DTL
 SN 4556



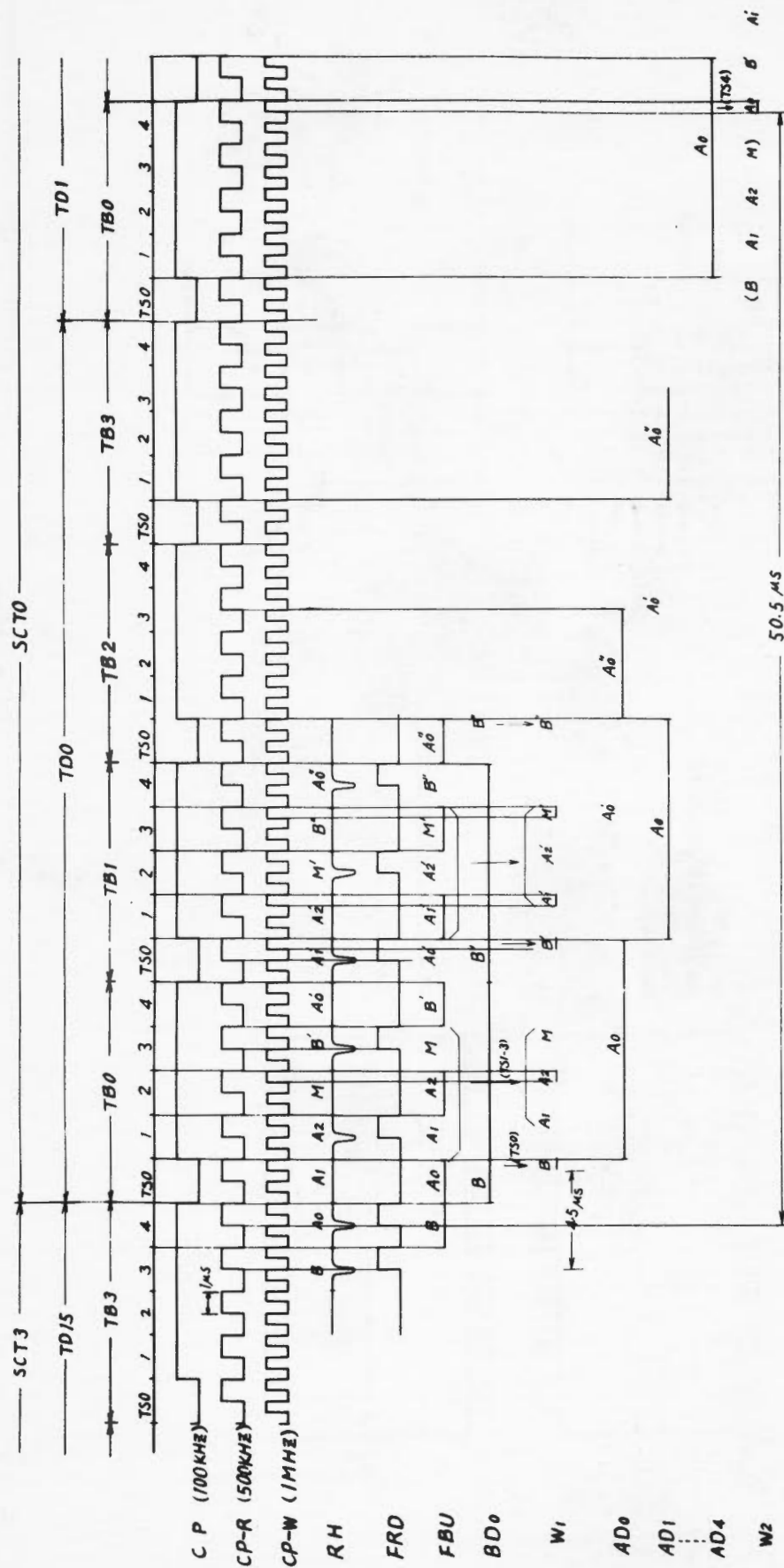
SN 4553



SN 4553 (X 65-7007) DTL
 SN 3920 (X 65-7003) DTL

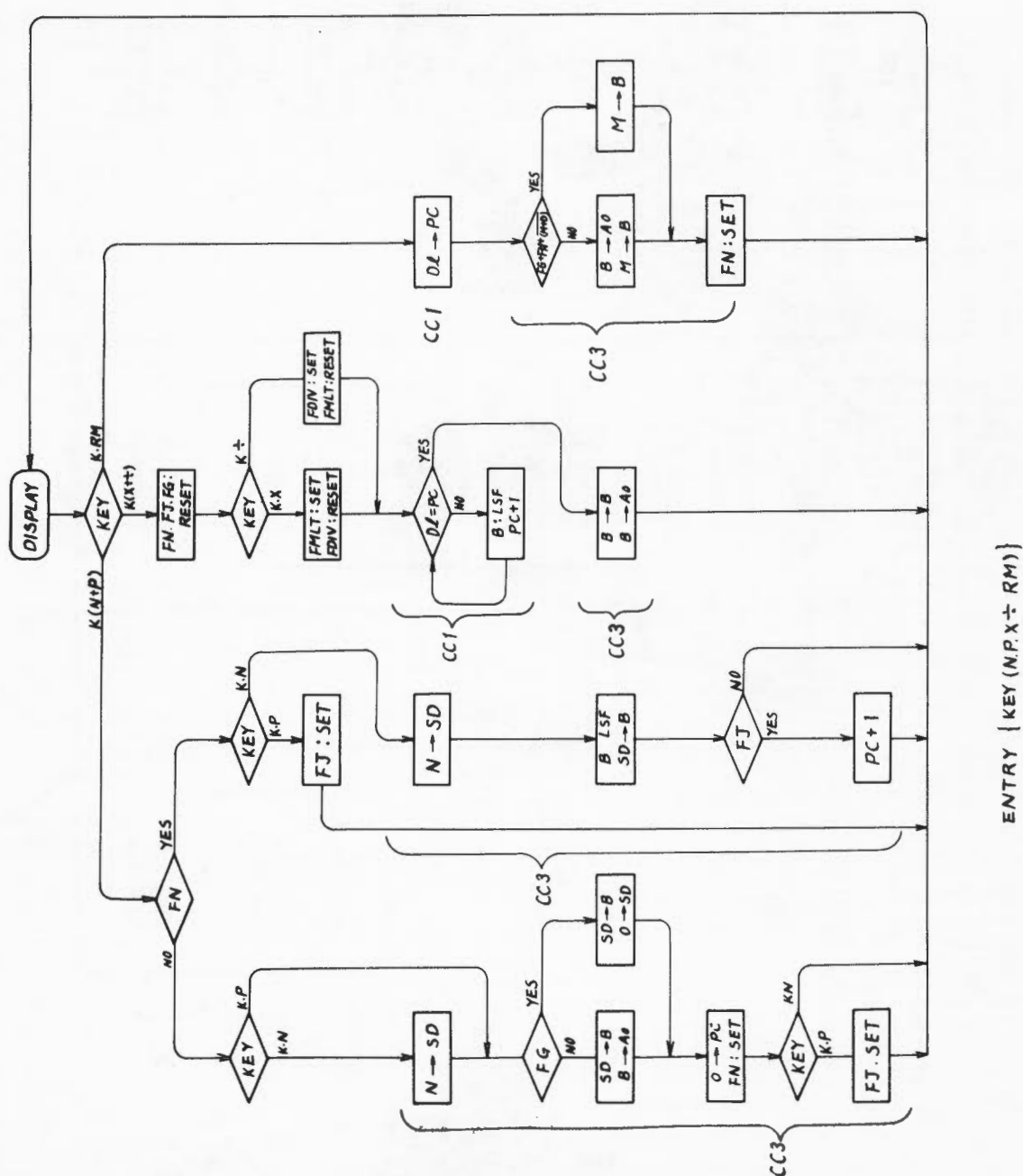
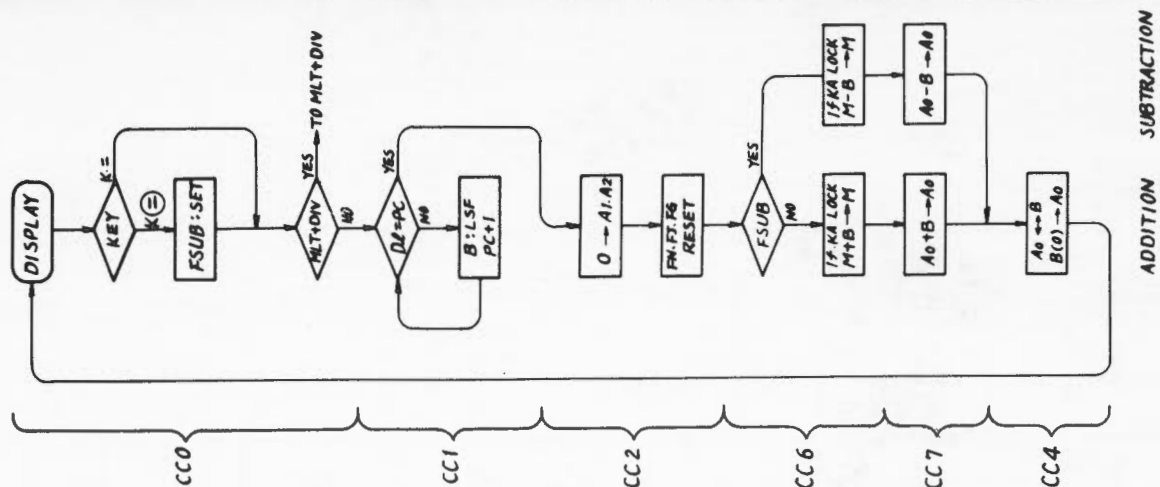


SN 7400 TTL

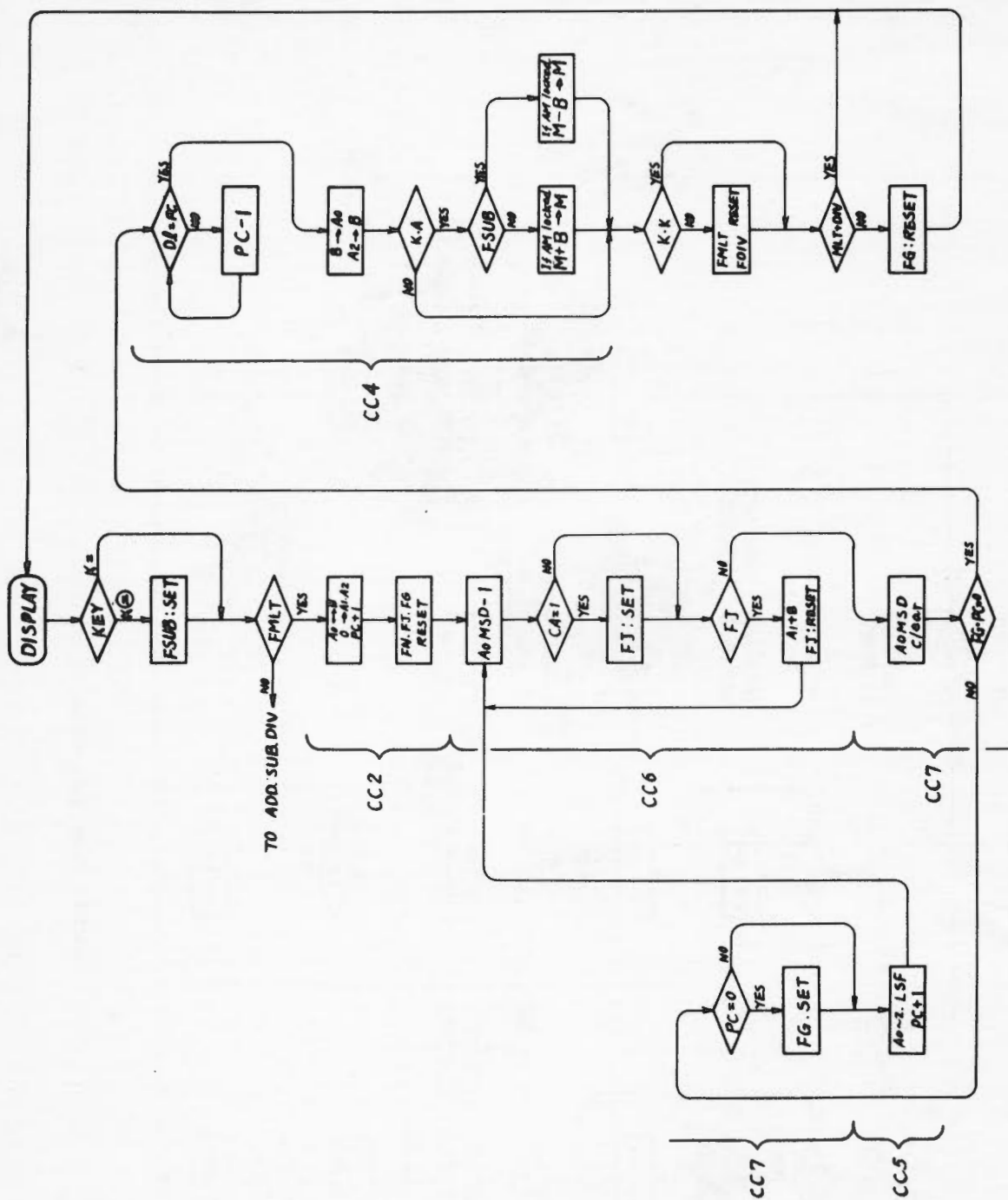


	R.H out				BD0	AD0	W1	W2
SCT0	B	A0	A1	A2	M	B'	B A1 A2 M	A0
SCT1	B	A1	A2	M	A0	B'	B A2 M A0	A1
SCT2	B	A2	M	A0	A1	B'	B M A0 A1	A2
SCT3	B	M	A0	A1	A2	B'	B A0 A1 A2	M

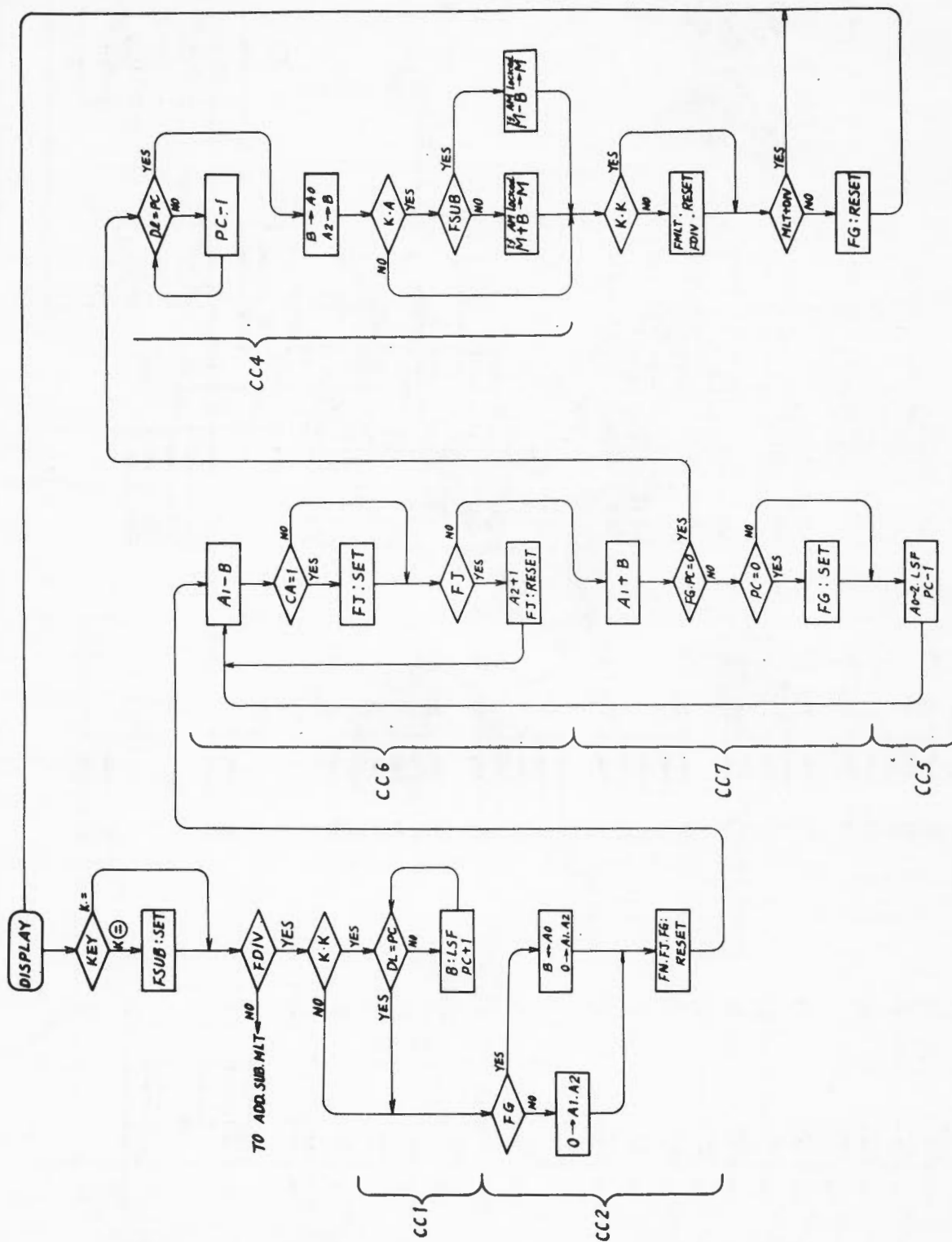
TIMING CHART

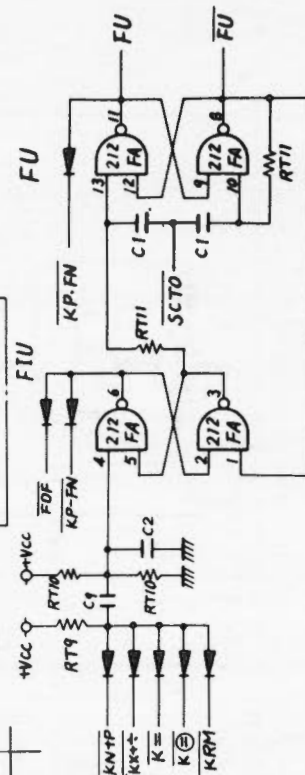
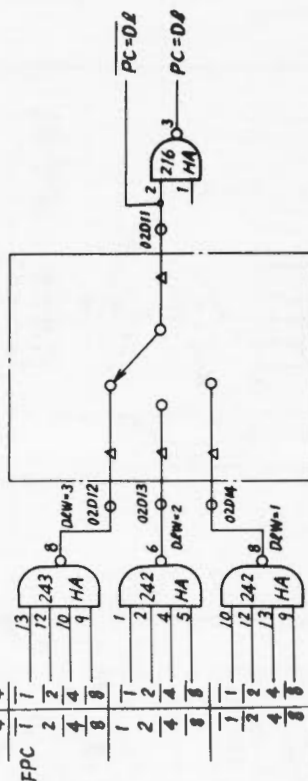
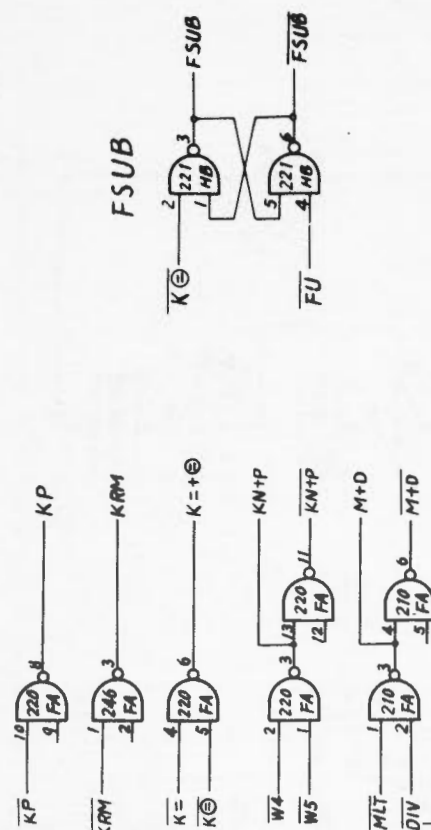
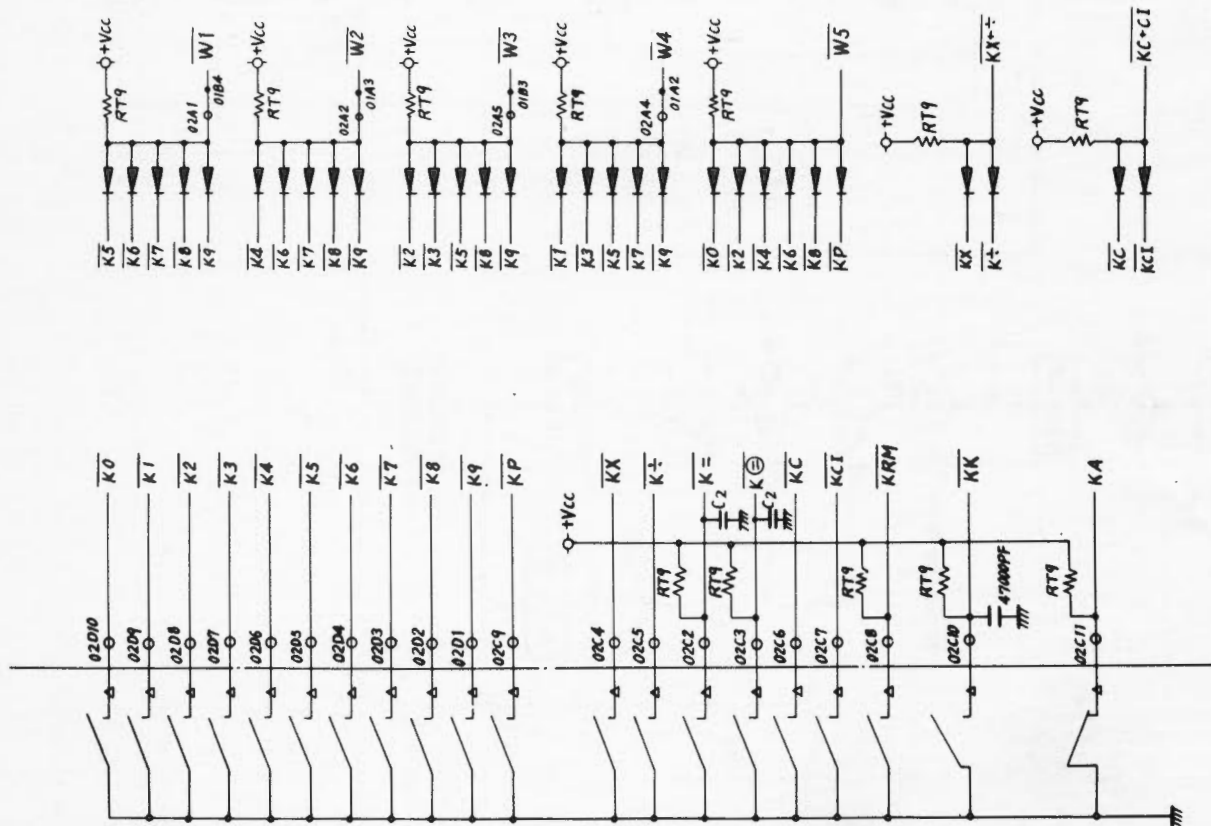


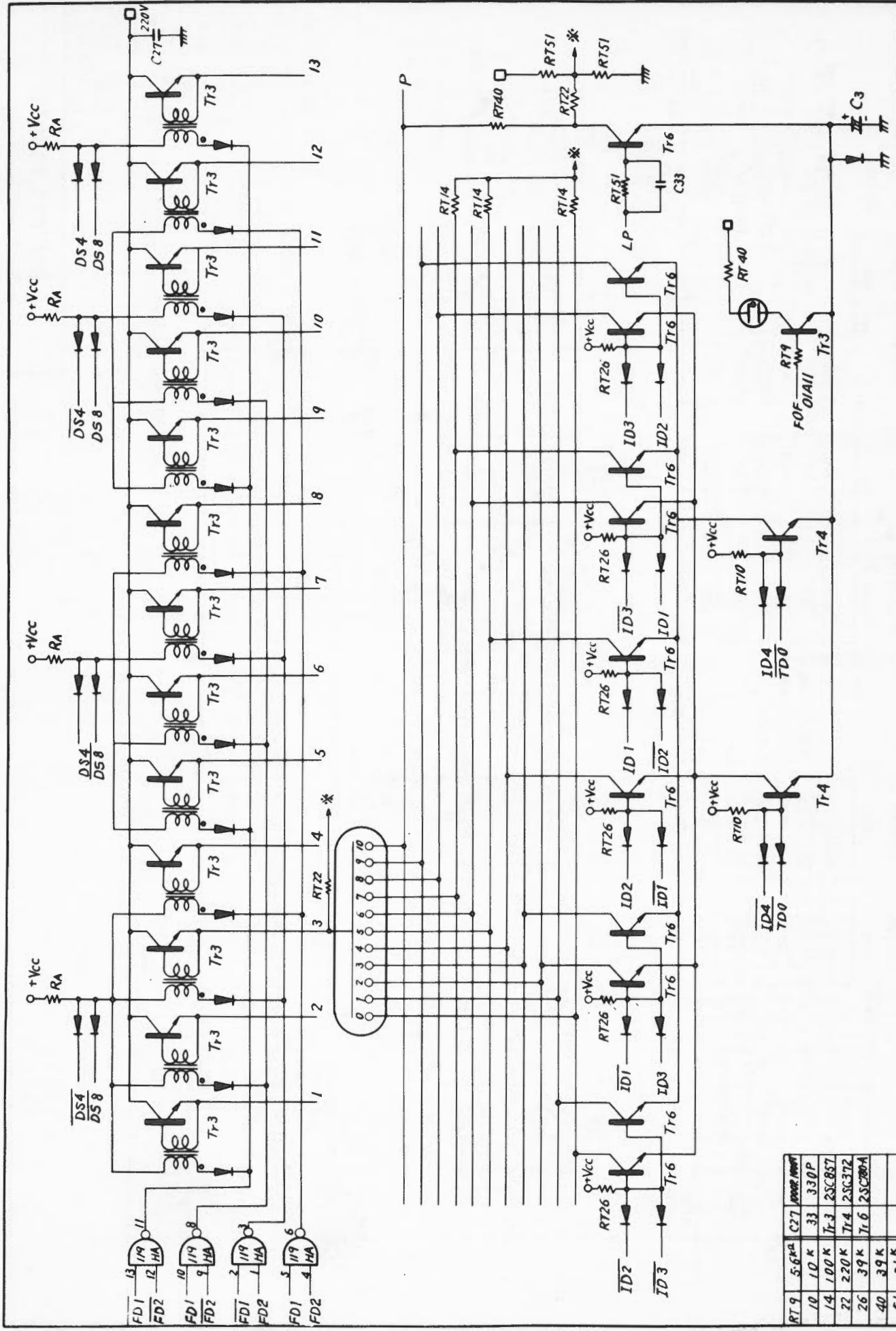
FLOW CHART, ENTRY, ADD & SUB



FLOW CHART MLT

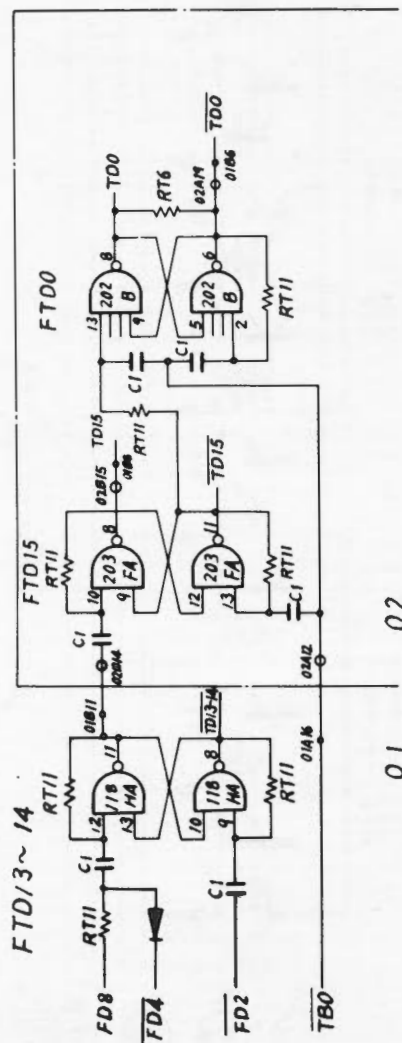
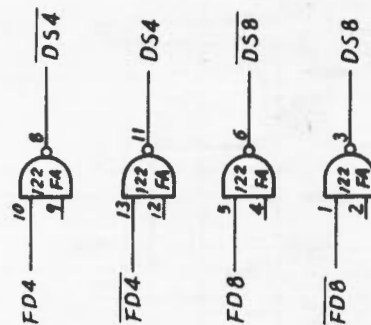
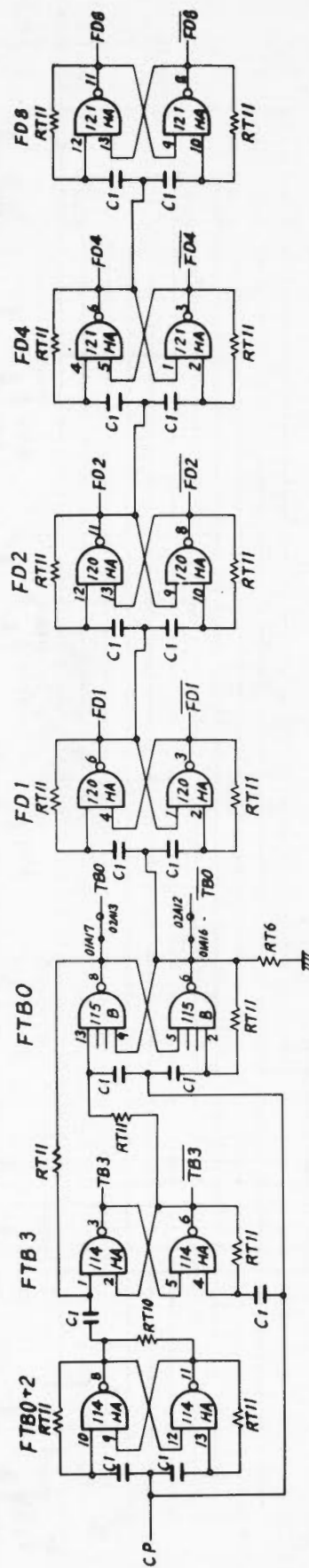
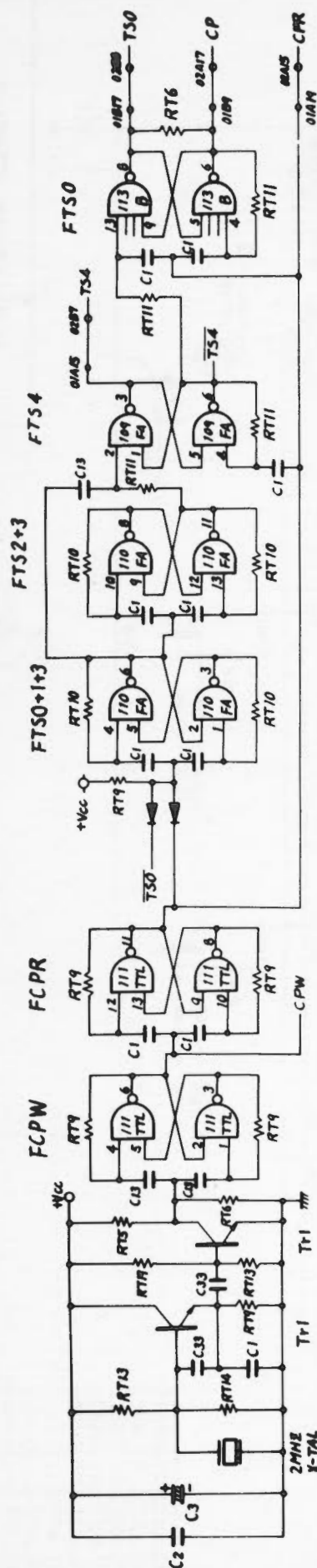






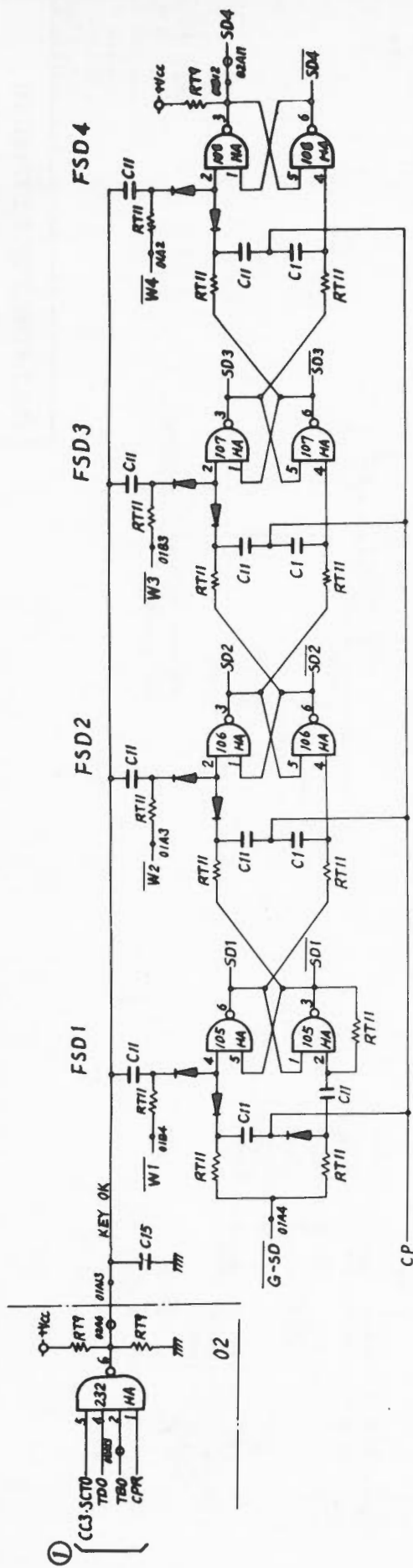
RT 9	5.6K	C27	100K	100K
10	10 K	33	330 P	
14	100 K	Tr 3	2SC857	
22	220 K	Tr 4	2SC372	
26	39 K	Tr 6	2SC200A	
40	39 K			
51	51 K			
RA	3.9K			

INDICATION CIRCUIT

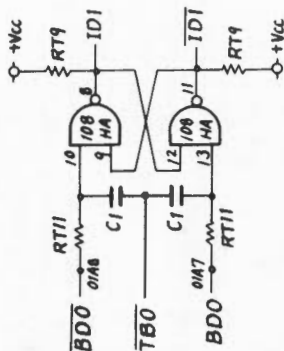


RT5	18KR
6	1 K
7	27K
8	56K
10	10K
11	22K
13	56K
14	100K
19	15K
C1	47PF
2	4700P
3	30.410V
13	33P
33	330P
1	25C61

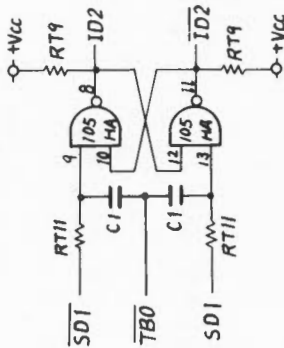
OSC FTS, FD & TD



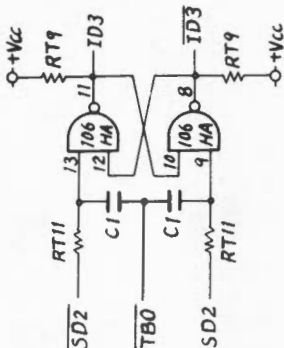
FID1



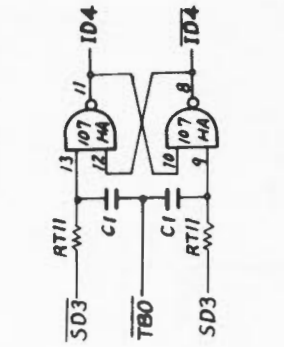
FID2

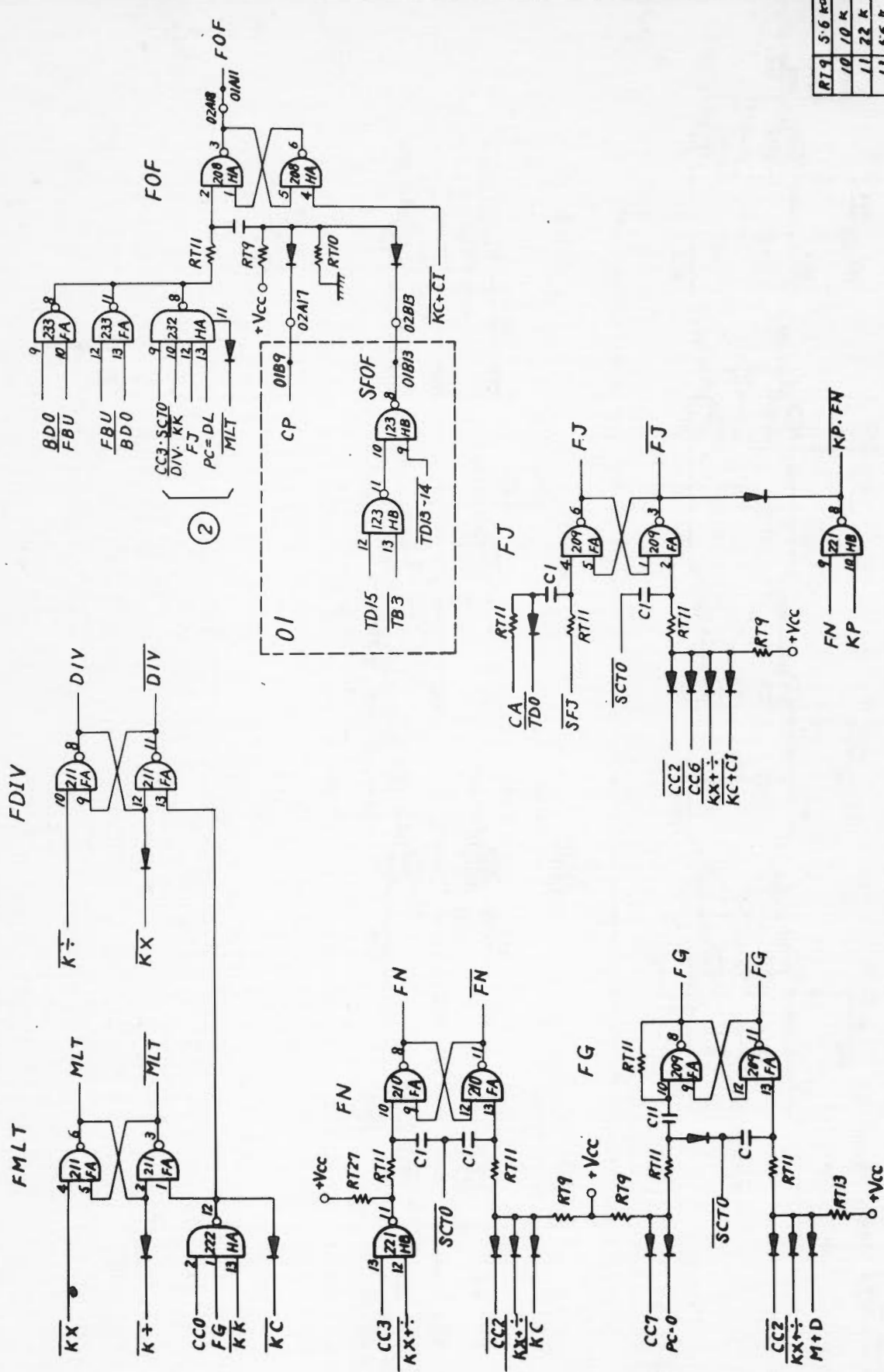


FID3



FID4

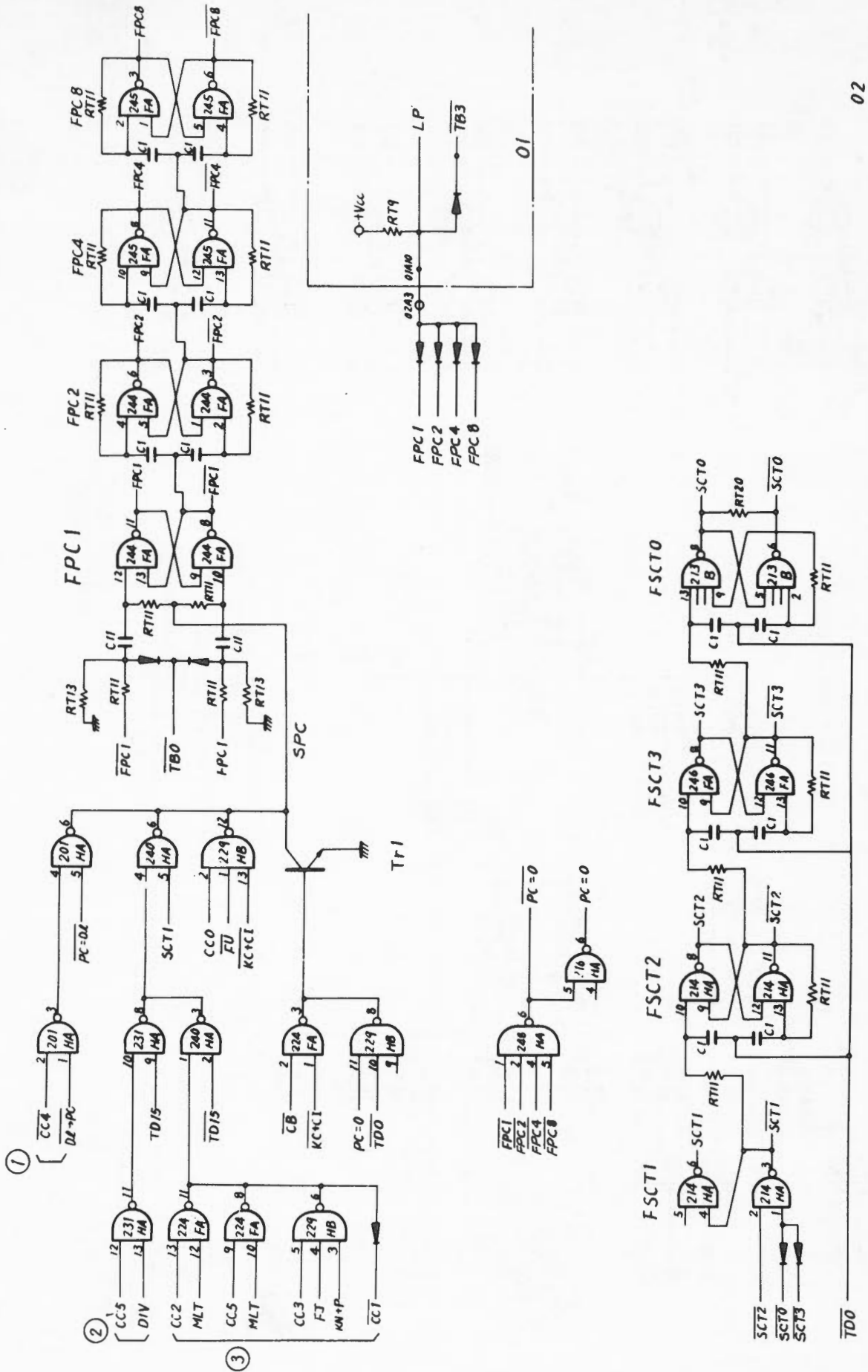




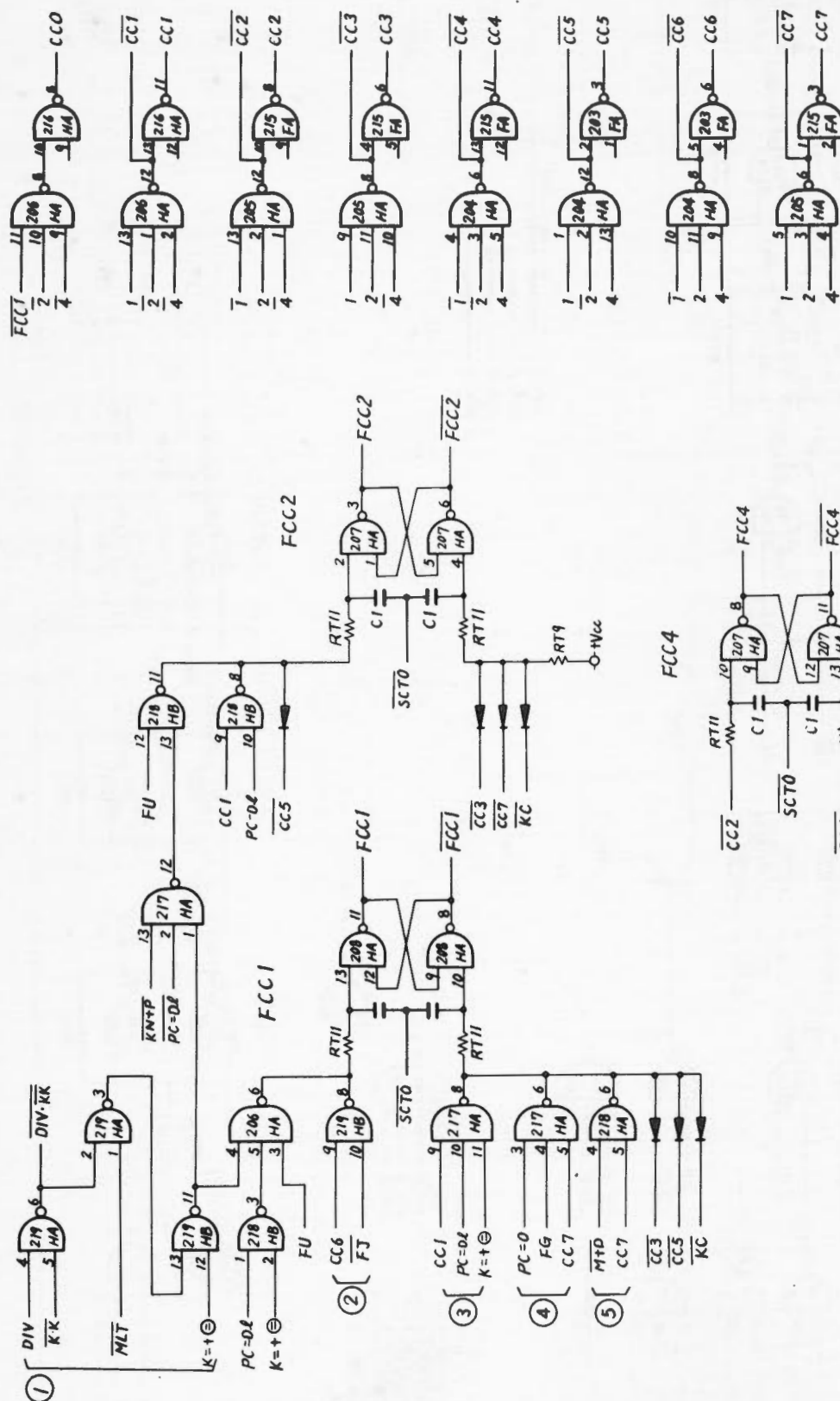
02

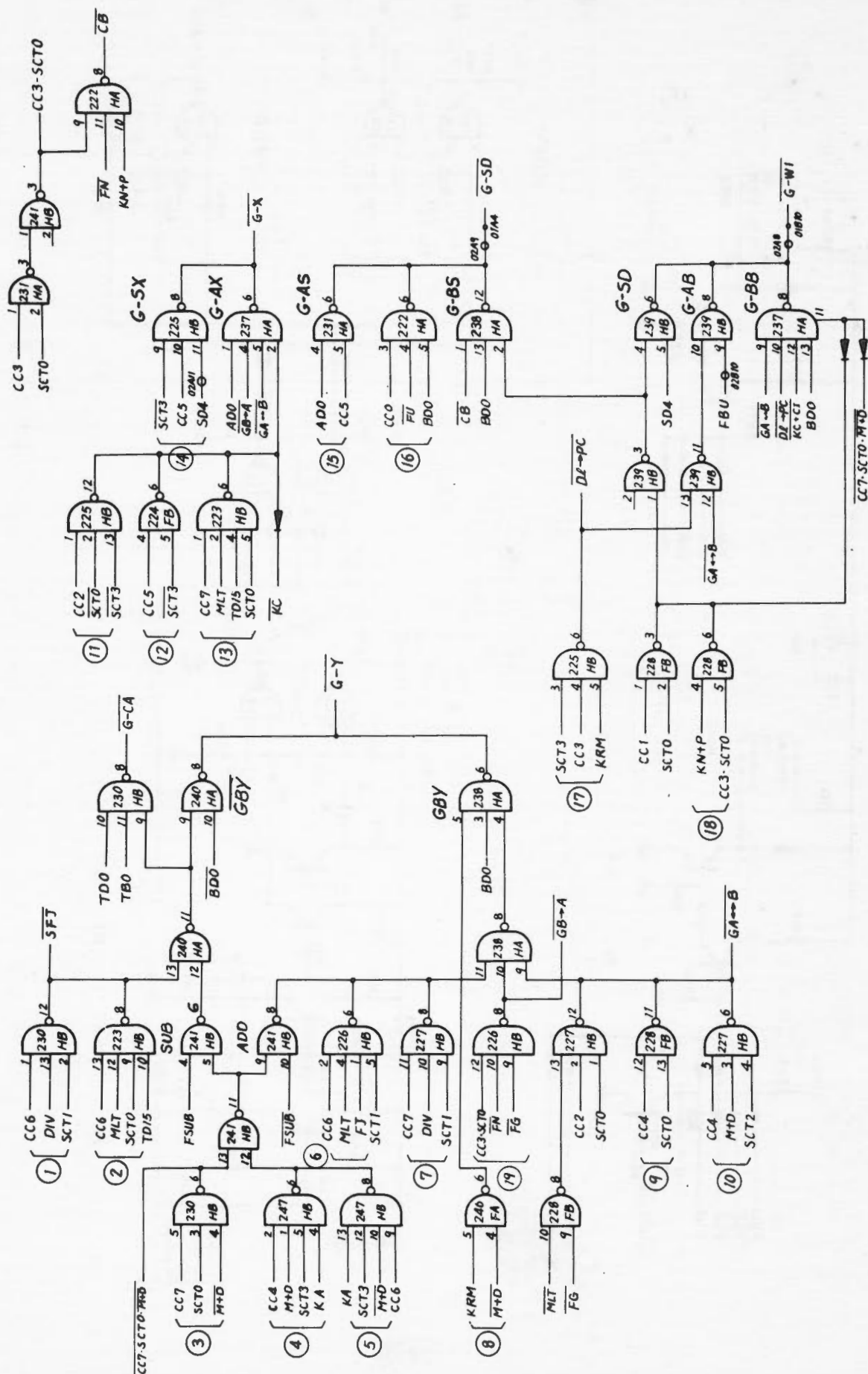
FMLT, FDIV, FOF, FJ, FN & FG

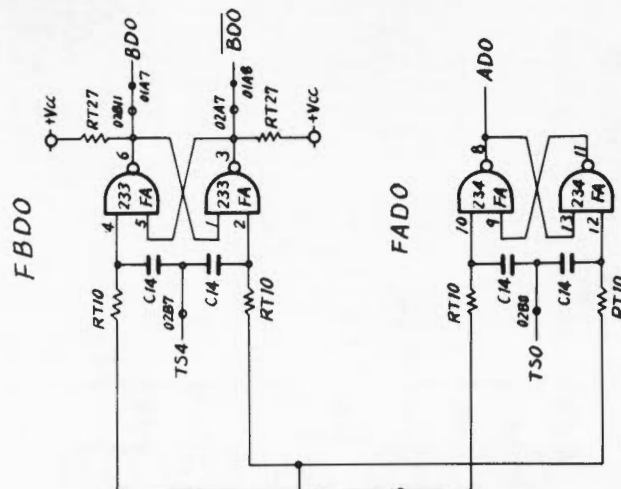
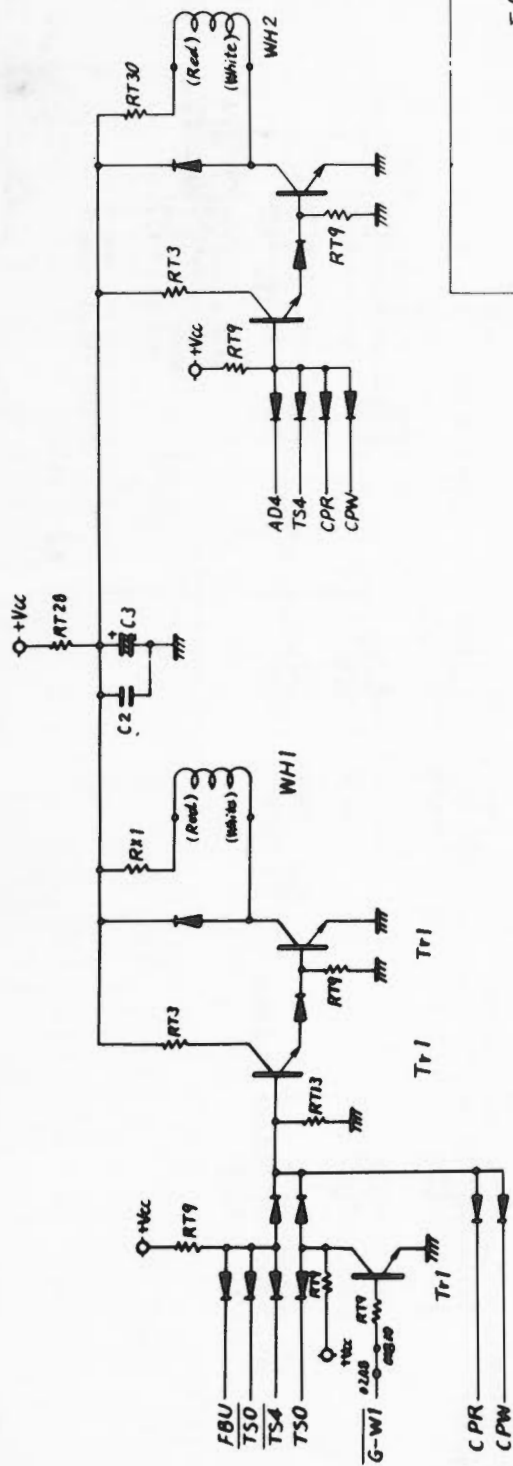
RT9	5.6 kΩ
10	10 kΩ
11	22 kΩ
13	56 kΩ
27	22 kΩ
C1	47 pF
11	82 pF



PC & SCT

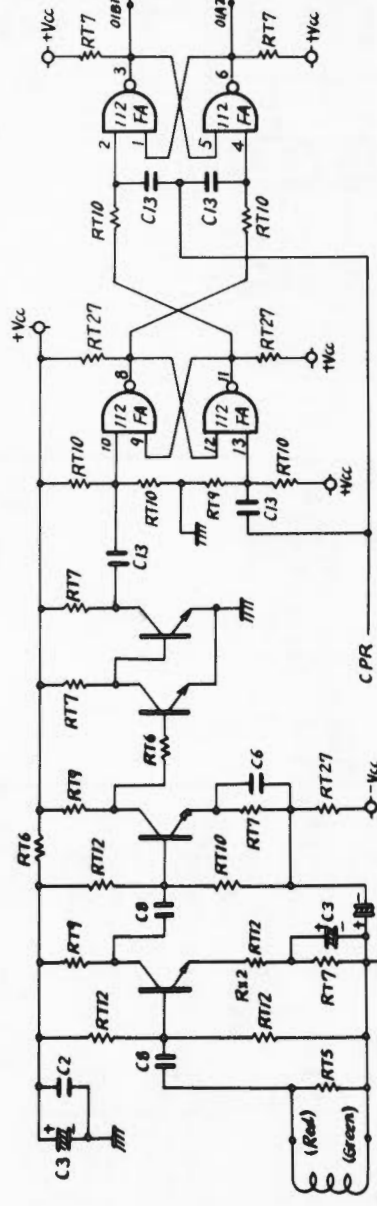






FBU

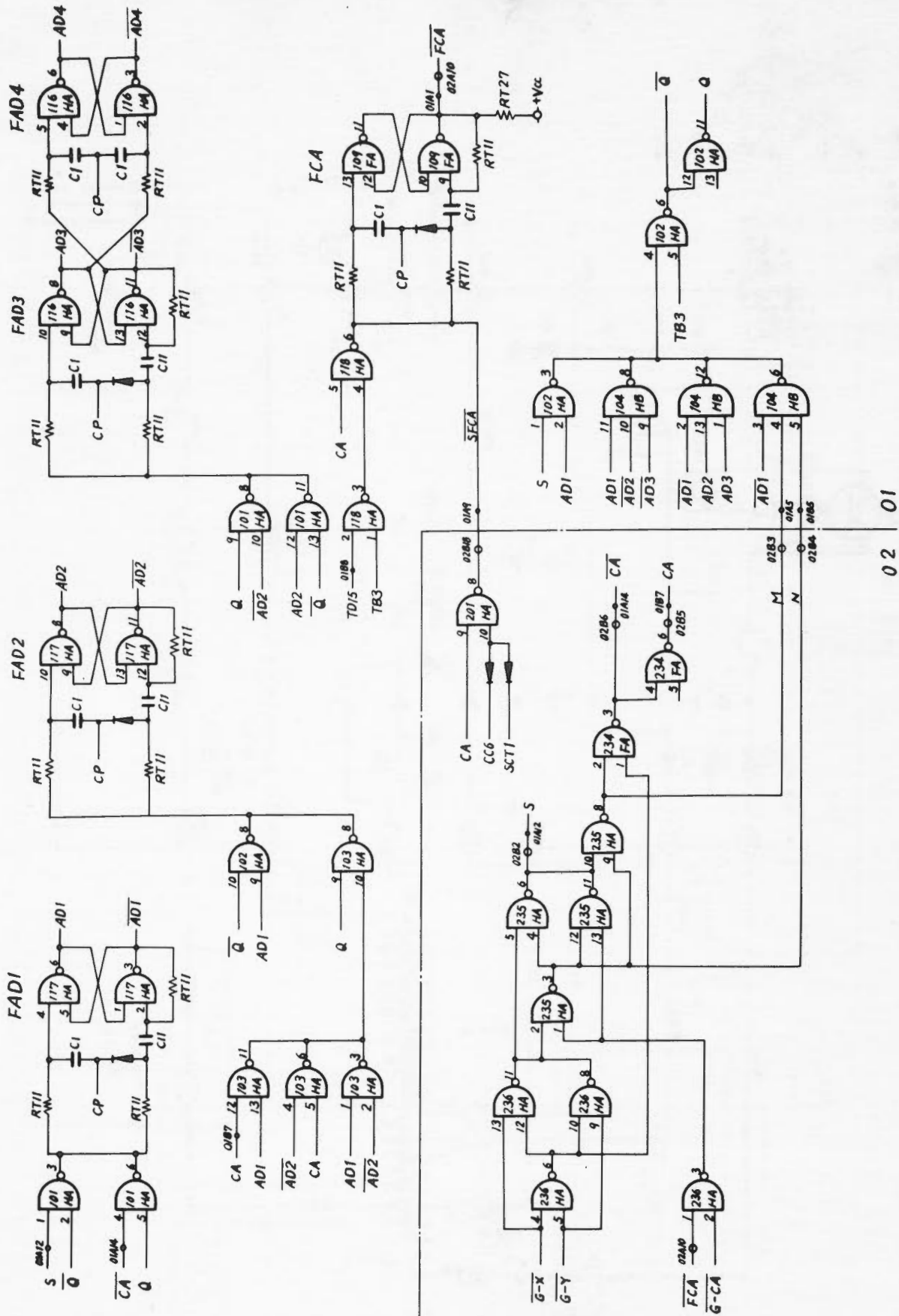
FR



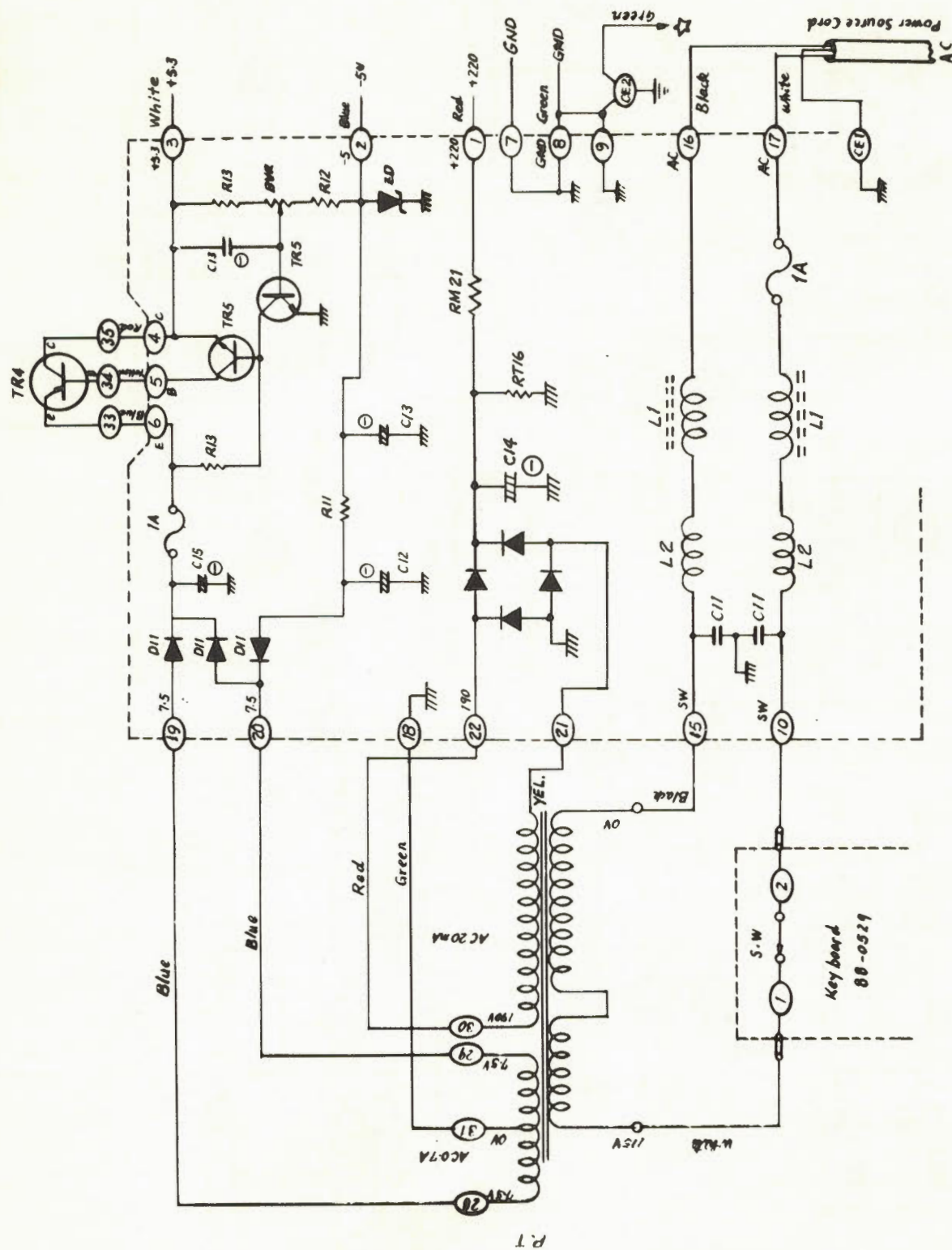
Tr1 Tr1 Tr1 Tr1

01 02

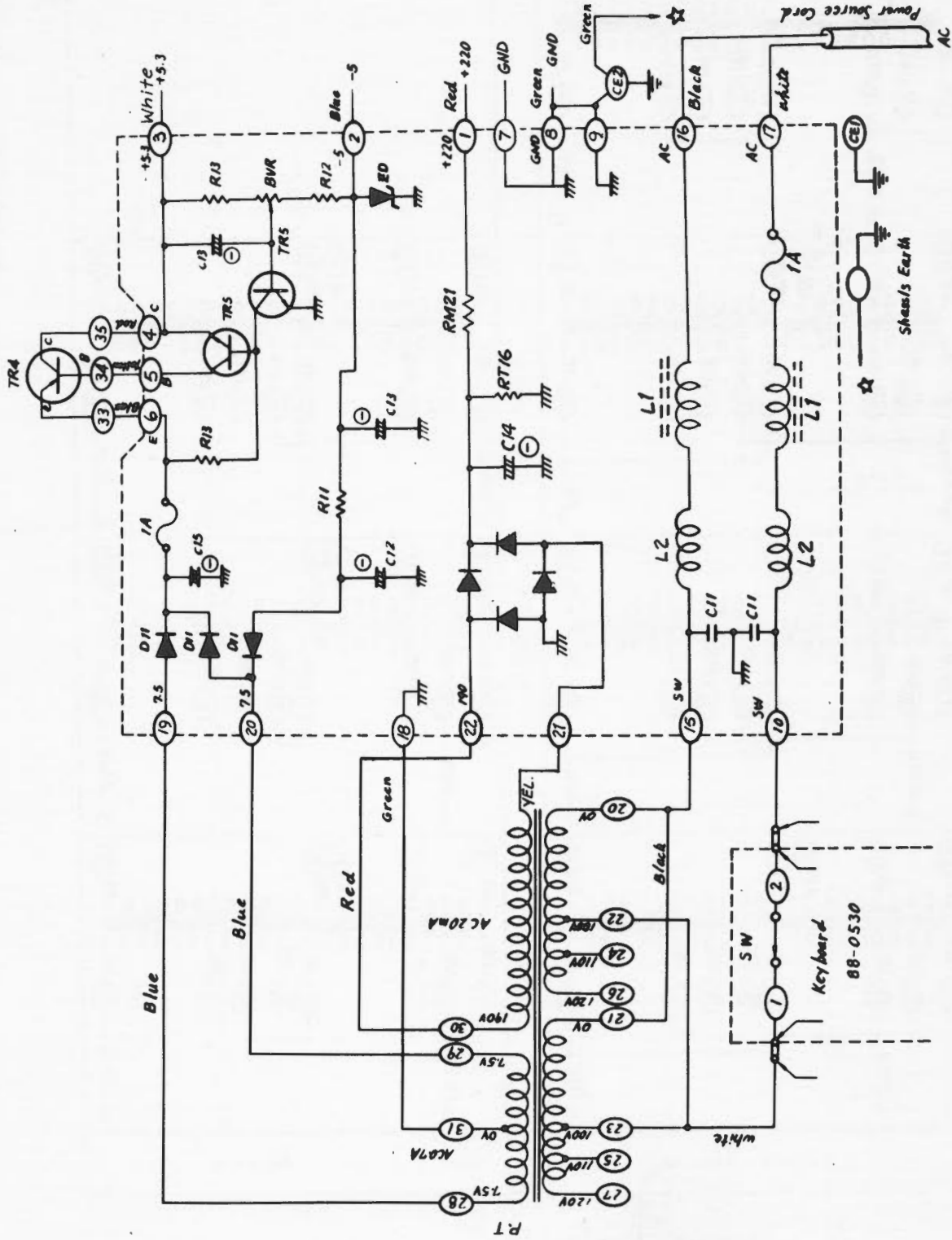
WRITE & READ AMP



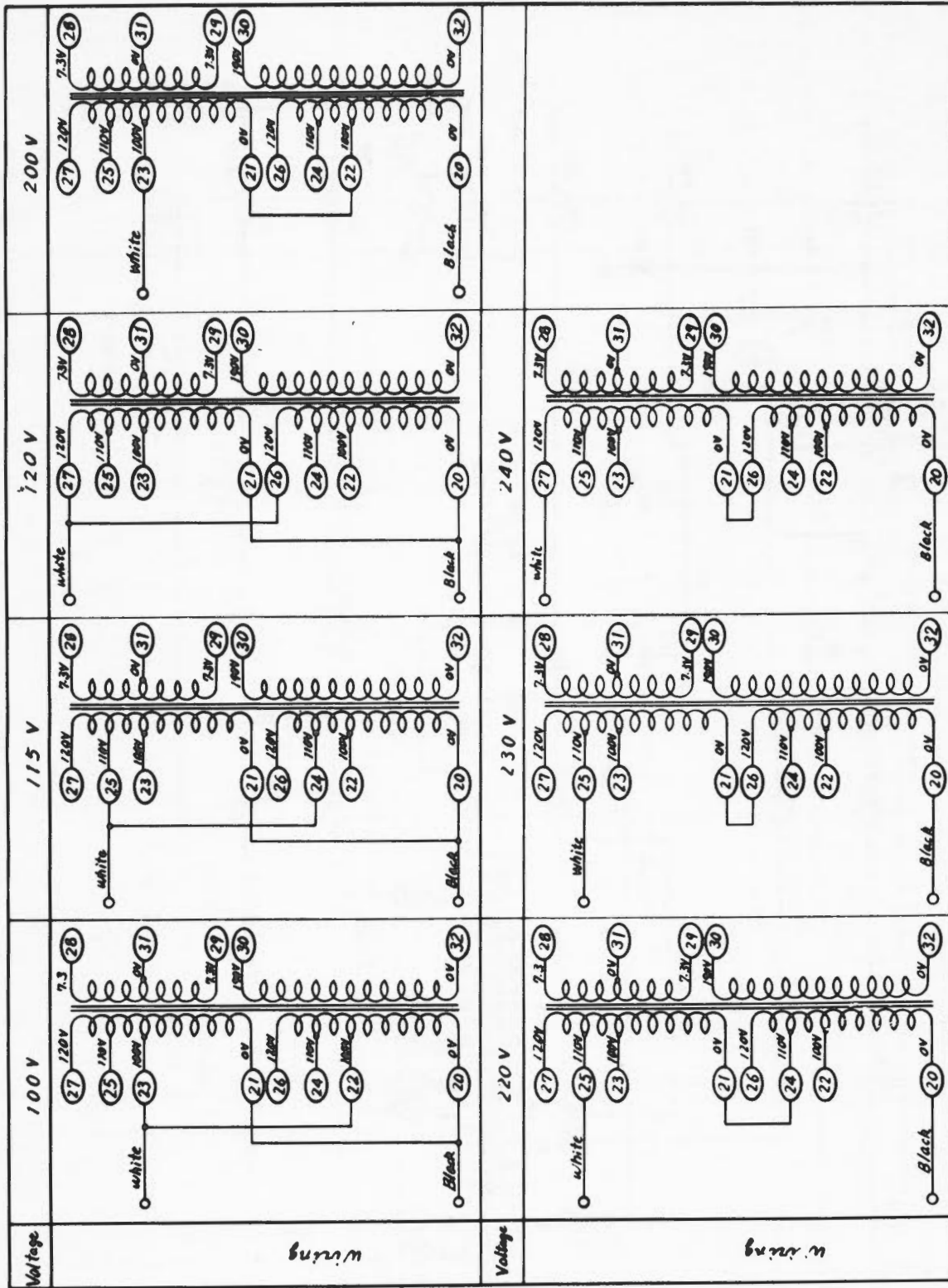
ADDER & FAD



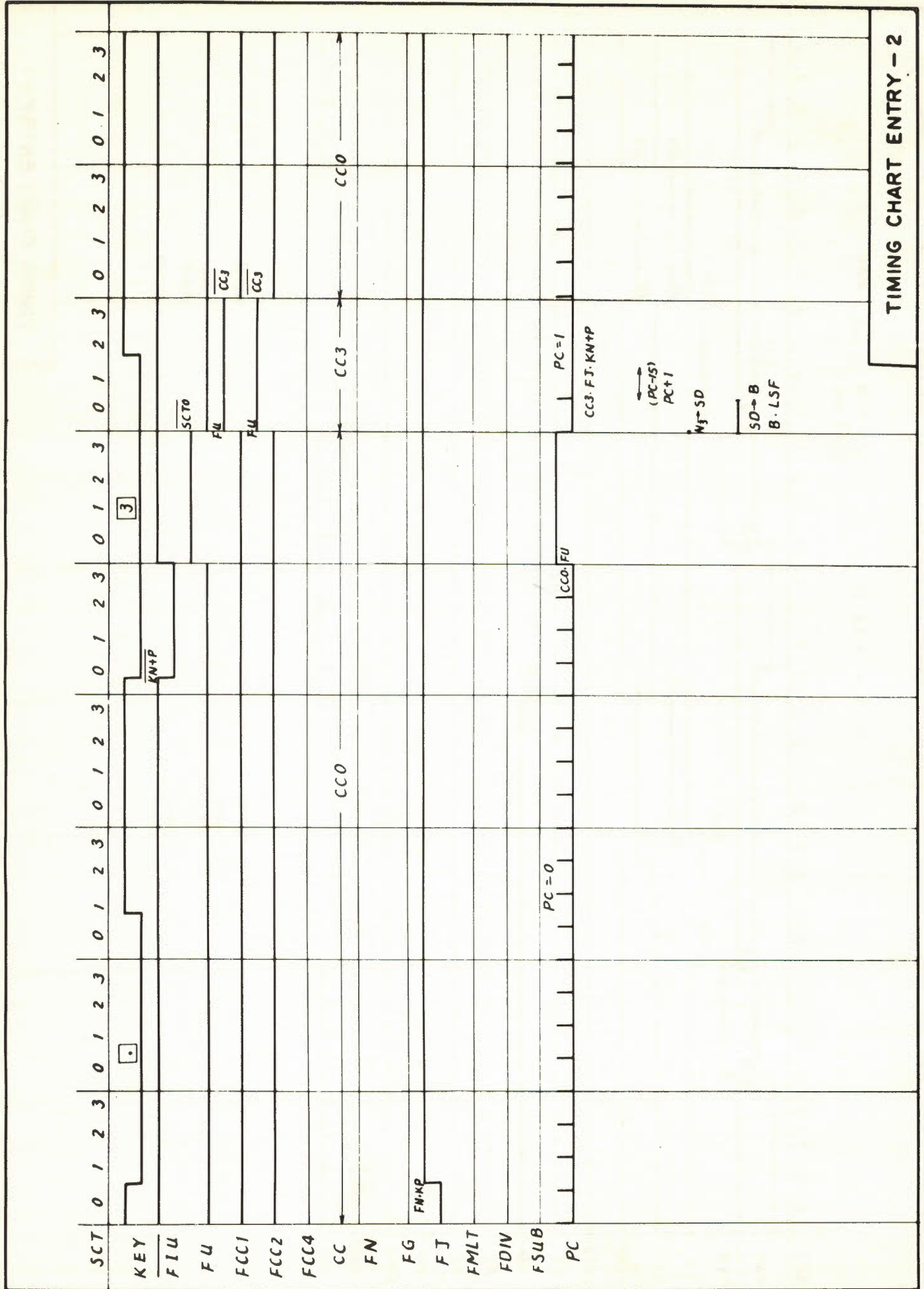
POWER SUPPLY CIRCUIT (115VUL)

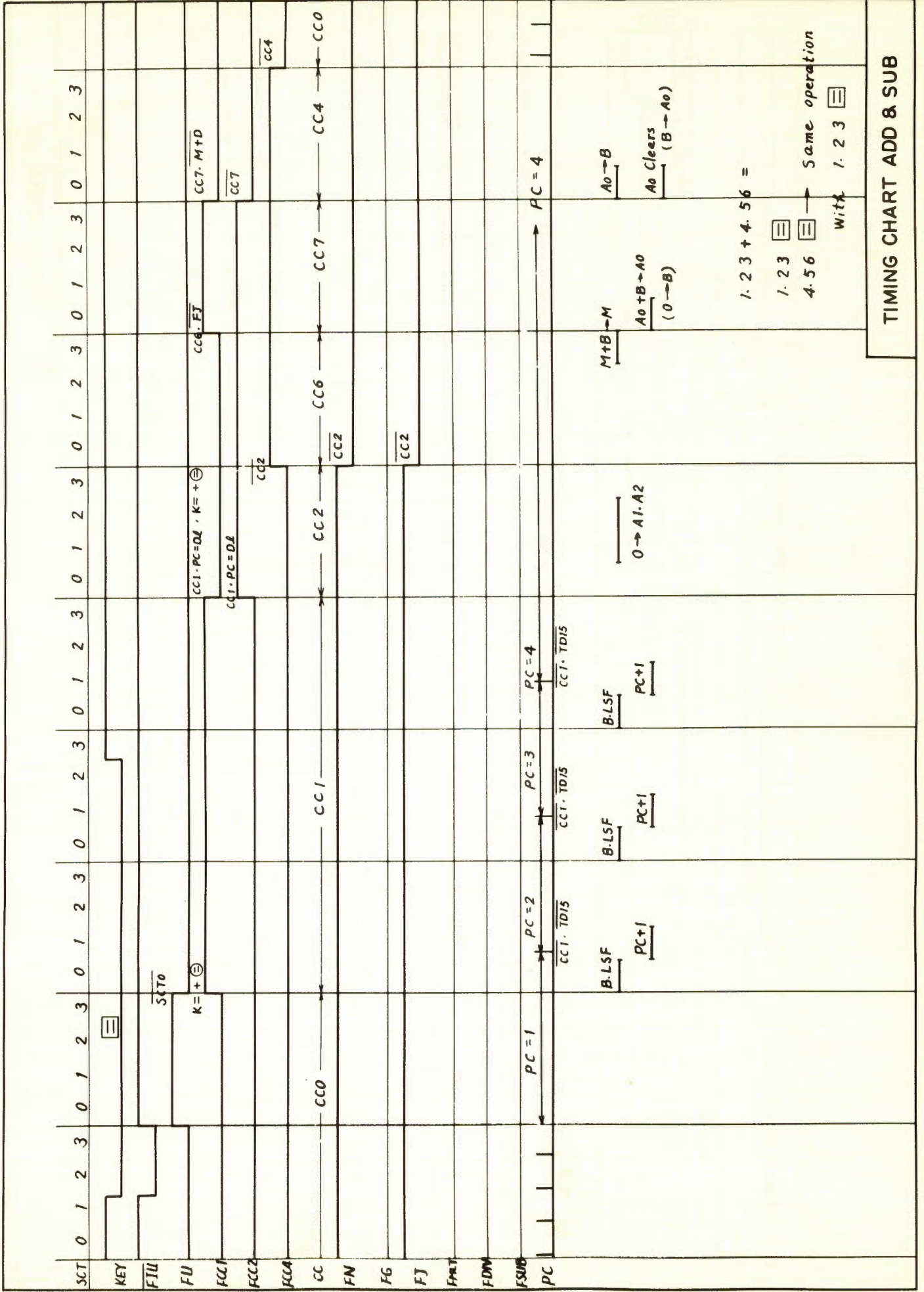


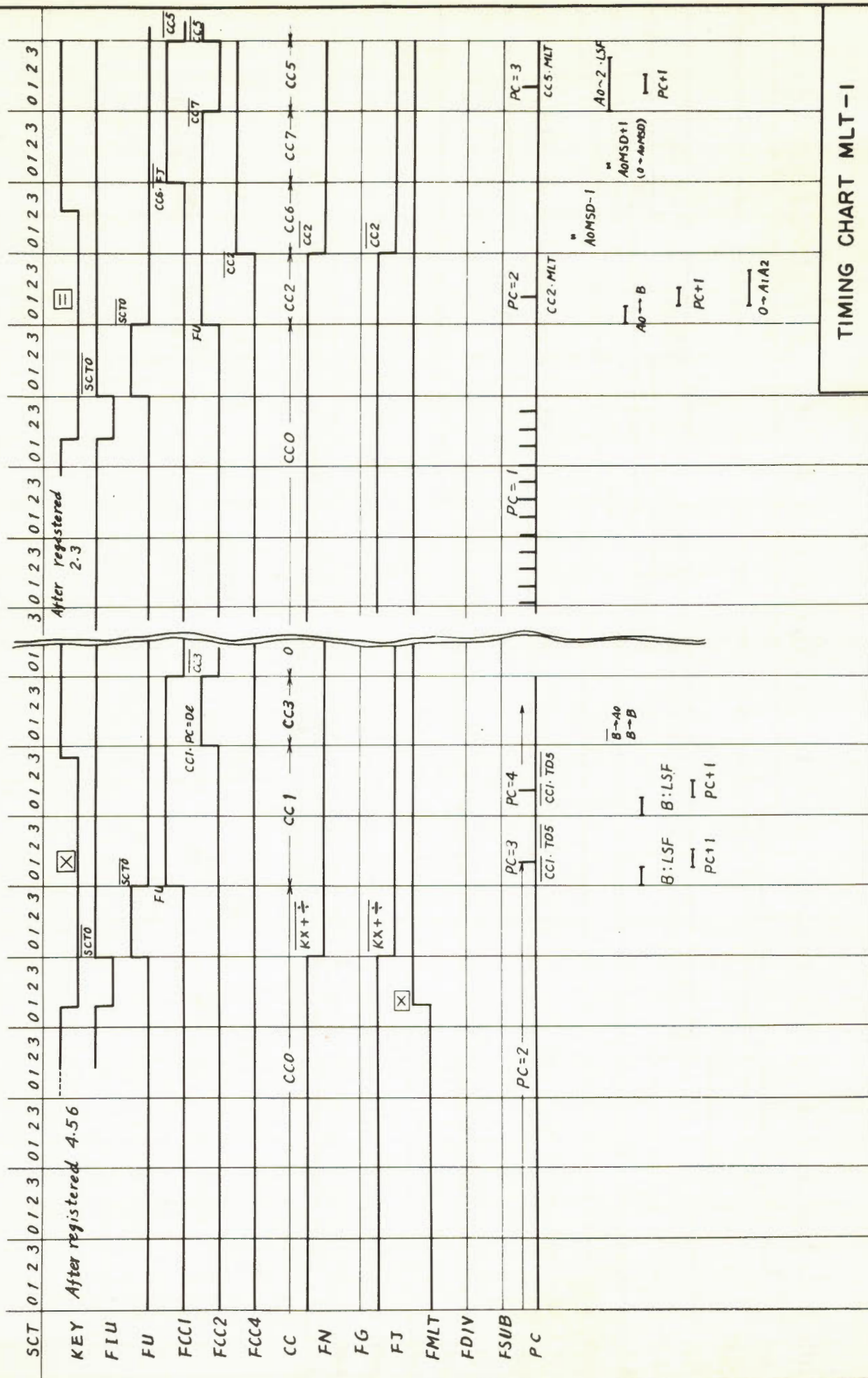
POWER SUPPLY CIRCUIT (EXCEPT IISVUL)



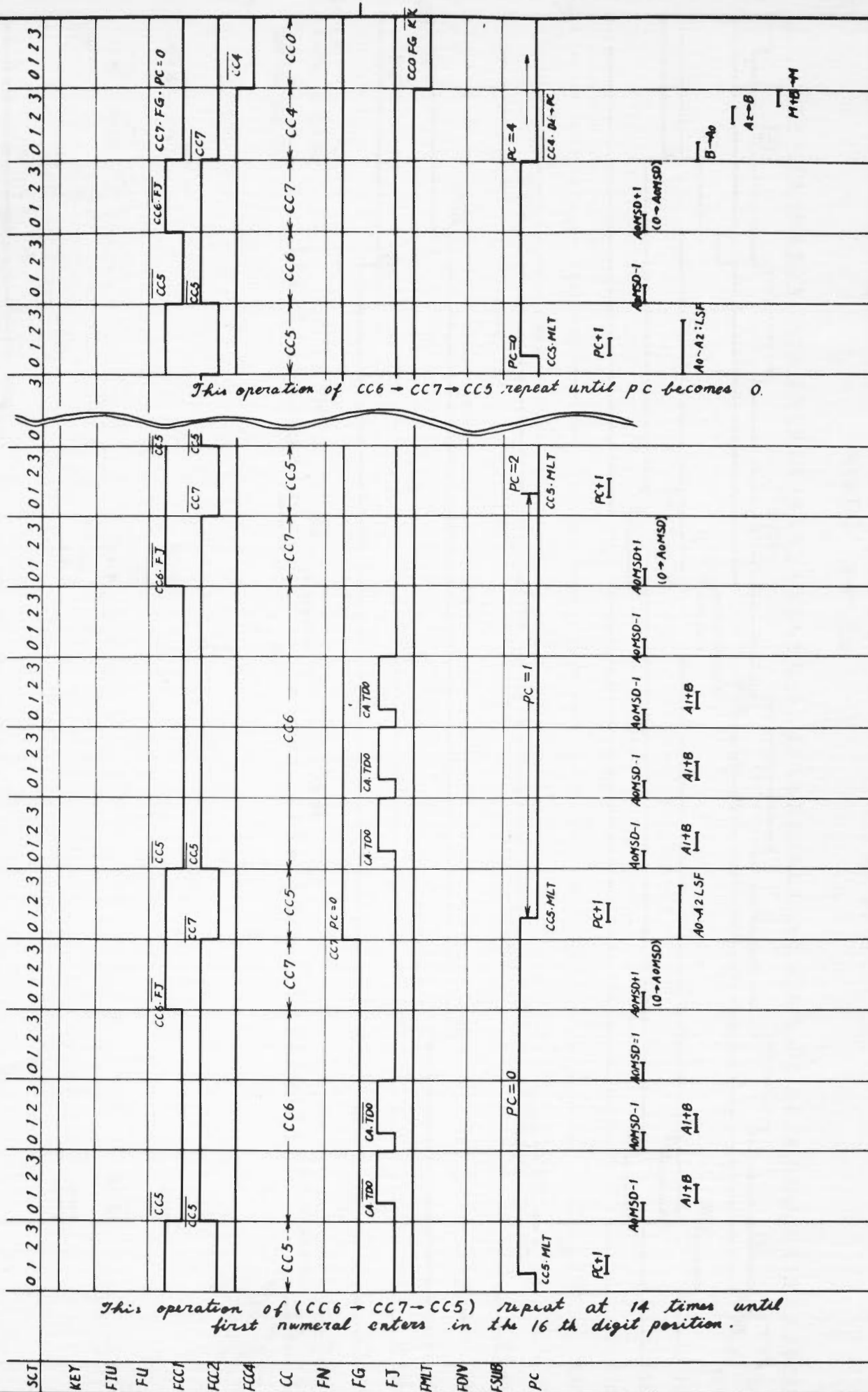
TRANSFORMER TERMINAL DIAGRAM







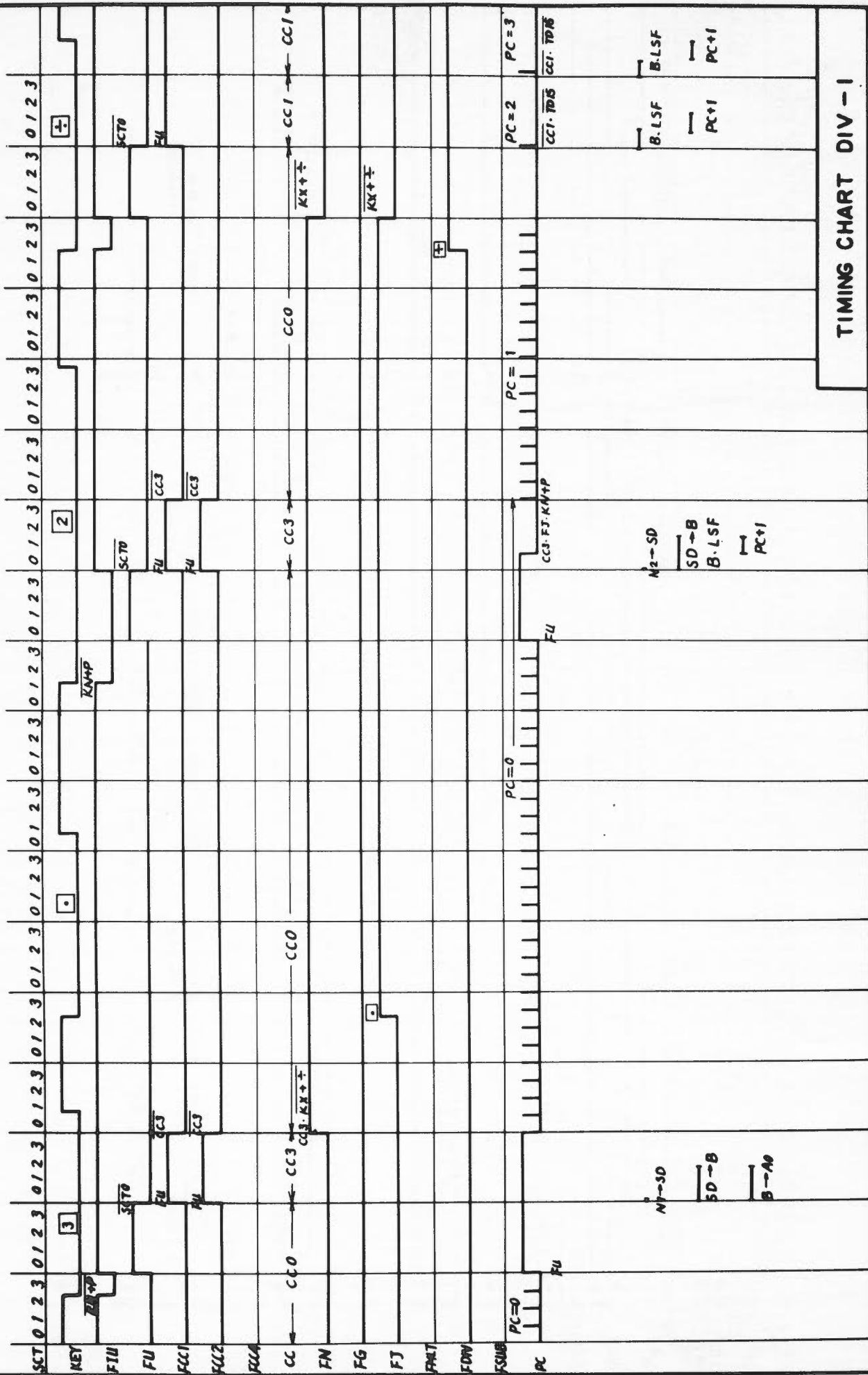
TIMING CHART MLT - 2



SGT KEY FTU FU FCCF FCCZ FCCA CC FN FG FJ FHLT FDNV FSUB PC

$$3.2 \div 4.56 =$$

DI = 4
AM ON K OFF

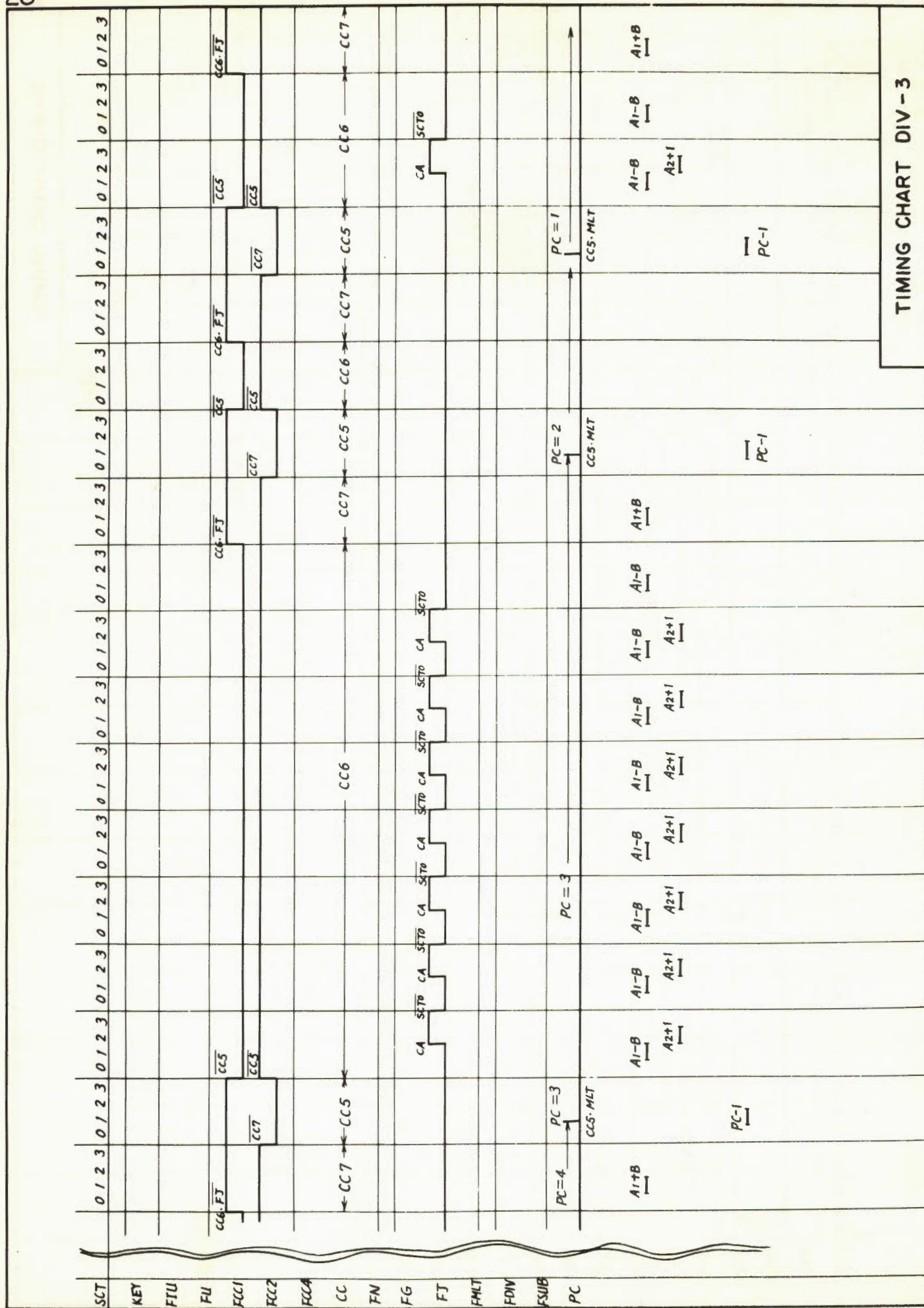


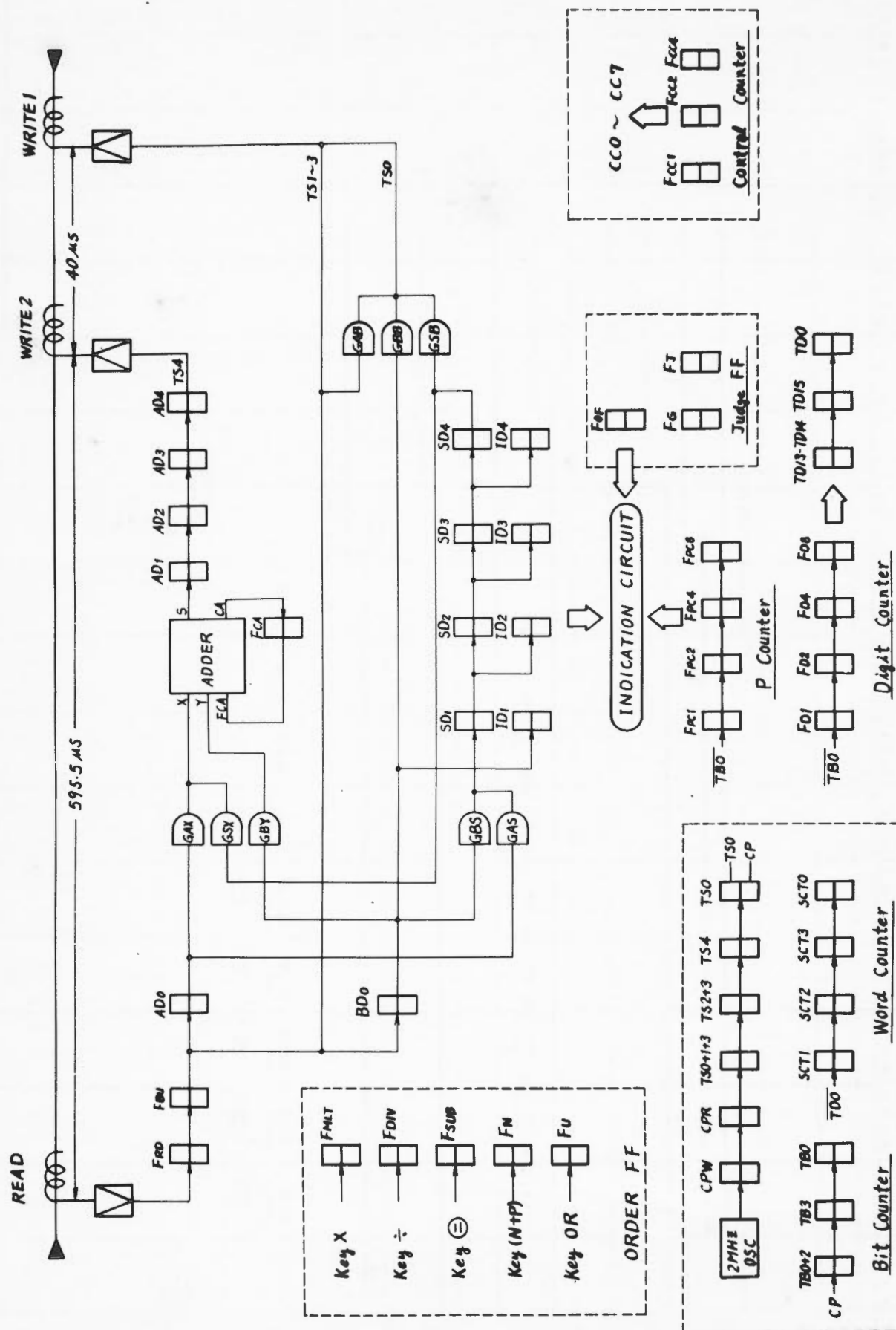
SCT	0	1	2	3	0	1	2	3	0	1	2	3	0	1	2	3	0	1	2	3	0	1	2	3	0	1	2	3
KEY																												
F1U																												
FU																												
FCC1																												
FCC2																												
FCC4																												
CC																												
FN																												
FG																												
FJ																												
FMLT																												
FDIV																												
FSUB																												
PC																												

after 4.56 are entered

3	0	1	2	3	0	1	2	3	0	1	2	3	0	1	2	3	0	1	2	3	0	1	2	3	0	1	2	3	0	1	2	3	0	1	2	3																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																												
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TIMING CHART DIV - 2





BLOCK DIAGRAM